

Monday December 7th, 2015

Time	Event						
9:00-10:00	Conference Opening						
10:00-11:00	Keynote Speech						
11:00-11:20	Coffee Break						
11:20-13:00	Room 1	Room 2		Room 3		Room 4	
	A1L-A	A1L-B		A1L-C		A1L-D	
	Analog Circuits I Mohamed Watheq Ali Kamel El-Kharashi	Bioengineering Circuits and Systems I Amir. M. Sodagar, Ricardo Reis		Applications of DSP and Security Techniques Gamal Fahmy, Sergio Bampi		Ultra-Low Voltage Digital Circuits Fernando Gehm Moraes	
13:00-13:50	Launch						
13:50-14:40	Plenary Lecture						
14:50-16:30	A2L-A	A2L-B		A2L-C		A2L-D	
	Low-Power Analog Circuits Hervé Barthelemy	RF Circuits and Systems Nicholas Preyss		Logic Circuits and Memories Sergio Bampi		Resiliency and Reliability Fernando Moraes	
	Coffee Break						
16:50-17:50	Poster Sessions						
	A3P-F	A3P-G	A3P-H	A3P-J	A3P-K	A3P-L	Forum
	Analog Circuit Techniques II Sameh A. Ibrahim	Digital Signal Processing for Communication Alexios Balatsoukas-Stimming	Nonlinear and Neural Systems Elena Blokhina, Mohammed Fouda	Bioengineering Circuits and Systems II Mohamed Abd El Ghany, Mohamad Sawan	Circuits and Systems for Communication Raafat Lababidi	Student Papers Contest Ahmed Radwan, Hossam Fahmy	Inno-vent Egypt Ahmed Madian, Bassem Abdullah, Ahmed Allam
19:00-21:00	Conference Reception						

Tuesday December 8th, 2015

Time	Event						
9:00-10:00	Honoring Ceremony						
10:00-11:00	Keynote Speech						
11:00-11:20	Coffee Break						
11:20-13:00	Room 1	Room 2		Room 3		Room 4	
	B1L-A	B1L-B		B1L-C		B1L-D	
	Communication Systems Nicholas Preyss	Data Converters Mohamed Abbas		Nonlinear Circuits and Systems Sergio Callegari, Sherif Amer		NoC and Multi-core Design M. Morsy Naeem Farag, M. Watheq Ali Kamel El-Kharashi	
13:00-13:50	Launch						
13:50-14:40	Plenary Lecture						
14:50-16:30	B2L-A	B2L-B		B2L-C		B2L-D	
	Analog Circuits and Systems for Wireless Communications Ahmed Khattab	Arithmetic and DSP Dionysis Reisis		Energy Harvesting, Power Management and Memristor Techniques I. Elfadel, A. Alharbi		CMOS Integrated Circuits for Physics Experiments M.DeMatteis,S.Damico	
	Coffee Break						
16:50-17:50	Poster Sessions						
	B3P-F	B3P-G	B3P-H	B3P-J	B3P-K	B3P-L	B3P-M
	Analog Circuit Techniques III Mohamed Zgaren	Test and Verification Ricardo Reis	Digital Circuits Ugur Cini	Analog Design Methodologies Richard Shi, Yehea Ismail	Masters & PhD Forum Ahmed Khattab	Emerging Technologies and Energy Harvesting Abdullah Alharbi, Ibrahim Elfadel	Sensors and Interfaces Thanos Stouraitis, Soliman Mahmoud
19:00-23:00	Conference Banquet						

Wednesday December 9th, 2015

Time	Event						
9:00-10:00	Award Ceremony						
10:00-11:00	Keynote Speech						
11:00-11:20	Coffee Break						
11:20-13:00	Room 1	Room 2		Room 3		Room 4	
	C1L-A	C1L-B		C1L-C		C1L-D	
	Analog and RF Circuits for Communications Mohamed Abbas	DSP Architectures for Multimedia & Communications Sergio Bampi, Gamal Fahmy		Reliable Systems: Modeling and Optimization Mohamed Dessouky		FPGAs and Architectural Exploration Ahmed Khattab, F.Gehm Moraes	
13:00-13:50	Launch						
14:00-15:40	C2L-A	C2L-B		C2L-C		C2L-D	
	VLSI Signal Processing for Communications Mounir Boukadoum	Digital Circuits, Systems and Tools Jorge Juan		NG-PON Network Elements & Photonic Components Ricardo Reis		Sensing and Sensor Networks Piero Malcovati, R. Carmona-Galán	
	Closing Ceremony						